



Docket No. P0284US-7

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: TING LI et al.

Serial No. 10/676,953

Filed: September 30, 2003

Title: LIGHT EMITTING DIODES WITH POROUS SiC SUBSTRATE AND METHOD FOR FABRICATING

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97(b)**

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), applicants bring to the attention of the Examiner the documents listed on the attached Form PTO-1449. This Information Disclosure Statement is being filed within three months of the filing of the above referenced application or before the mailing date of a first Office Action on the merits for the above-referenced application.

In accordance with the PTO procedure, (all patent applications filed after June 30, 2003), we have not enclosed the U.S. patent references cited on PTO form 1449.

Applicants respectfully request that the Examiner consider the listed documents and evidence that consideration of relevant portions thereof by making appropriate notations on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art". If the Examiner applies any of the documents as prior art

---

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

2/02/04  
Date


Marianne Middleton

against any claim in the application and applicants determine that the cited documents do not constitute "prior art" under United States law, applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

Respectfully submitted,

Dated: 2/2/04

  
Jaye G. Heybl  
Registration No. 42,661  
Attorney for Applicant

KOPPEL, JACOBS, PATRICK & HEYBL  
555 St. Charles Drive, Suite 107  
Thousand Oaks, California 91360  
(805) 373-0060  
M/J4-P0284US-7IDS-1.97(b).doc

(Use several sheets if necessary)

Group Art Unit

	1	APPLIED PHYSICS LETTERS, VOL. 79, NUMBER 15, OCT. 8, 2001, P. 2315-2317 "IMPACT OF TEXTURE-ENHANCED TRANSMISSION ON HIGH-EFFICIENCY SURFACE-TEXTURED LIGHT-EMITTING DIODES", WINDISCH et al.
	2	APPLIED PHYSICS LETTERS, VOL. 63, NO. 16, OCT. 18, 1993, P. 2174-2176, "30% EXTERNAL QUANTUM EFFICIENCY FROM SURFACE TEXTURED, THIN-FILM LIGHT-EMITTING DIODES", SCHNITZER et al.
	3	IEEE JOURNAL ON SELECTED TOPICS IN QUANTUM ELECTRONICS, VOL 8, NO. 2, MARCH/APRIL 2002, P. 248-255, "LIGHT-EXTRACTION MECHANISMS IN HIGH-EFFICIENCY SURFACE-TEXTURED LIGHT-EMITTING DIODES" WINDISCH et al.
	4	IEEE JOURNAL ON SELECTED TOPICS IN QUANTUM ELECTRONICS, VOL. 8, NO. 2, MARCH/APRIL 2002, P. 321-332, "HIGH BRIGHTNESS AlGaInP LIGHT-EMITTING DIODES", STREUBEL et al.
	5	IEEE TRANSACTIONS ON ELECTRON DEVICES, VOL 47, NO. 7, JULY/2000, p. 1492-1498 "40% EFFICIENT THIN-FILM SURFACE-TEXTURED LIGHT-EMITTING DIODES BY OPTIMIZATION OF NATURAL LITHOGRAPHY", WINDISCH et al.
	6	SPIE VOL 3938 (2000), LIGHT-EMITTING DIODES, MANUFACTURING, AND APPLICATIONS IV, INVITED PAPER, "NON-RESONANT CAVITY LIGHT-EMITTING DIODES", WINDISCH et al., P. 70-76
Examiner		Date Considered
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through		